Product Brief
Recorders, Switches, TAPS & Gateways

Telspandata.com/iGU

Gateways

Instrumentation Gateway - iGU

Configurable gateway between instrumentation systems, Ethernet networks and telemetry transmitters. Contains a layer 2/3 managed switch, FPGA & quad-core ARM CPU for data processing & protocol transforms

Features

- PCM Inputs/Outputs, NTSC Video Input
- IEEE-1588v2 Master/Slave, IRIG TCG
- Ethernet & NTSC Video to Chapter 10
- Ethernet & Chapter 10 Encoding to Chapter 7/HDLC PCM
- Chapter 7/HDLC PCM Decoding to Ethernet & CH10

Switches

Integrated Ethernet Switches - iES-16/12/8/6 are layer 2/3 managed gigabit Ethernet switches w/ end-node timing for demanding test instrumentation environments

Features

- IRIG 106 Chapter 7/HDLC PCM Encoder/Decoder
- IRIG-A/B/G Time Code Generator (GPS/PTP Source)
- IEEE-1588 PTP Clocks w/ Grand Master (GPS Source)
- IRIG DC/AM/1PPS Outputs
- Programmable Discrete Inputs/Outputs
- iES-12 & iES-16 contain a Switch Connected FPGA for Packet Level Data Processing

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TAP's

Remote TAP & Aggregation Gateway - RTAG

Based on the MITRs Tap & Interface Modules (TIM), the RTAG provides a distributed "TAP & Aggregate" capability for copper and optical links on an existing platform

Features

- Ethernet, Fibre Channel & IEEE-1394/AS5643 TAP's
- Optical & Copper
- Rx/Tx TAP to Tx Output or Aggregate Multiple TAP's to Tx Output
- Fibre to Copper Media Converter
- S-Group Message Filtering
- Chapter 7/HDLC PCM Encoder/Decoder

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Recorders

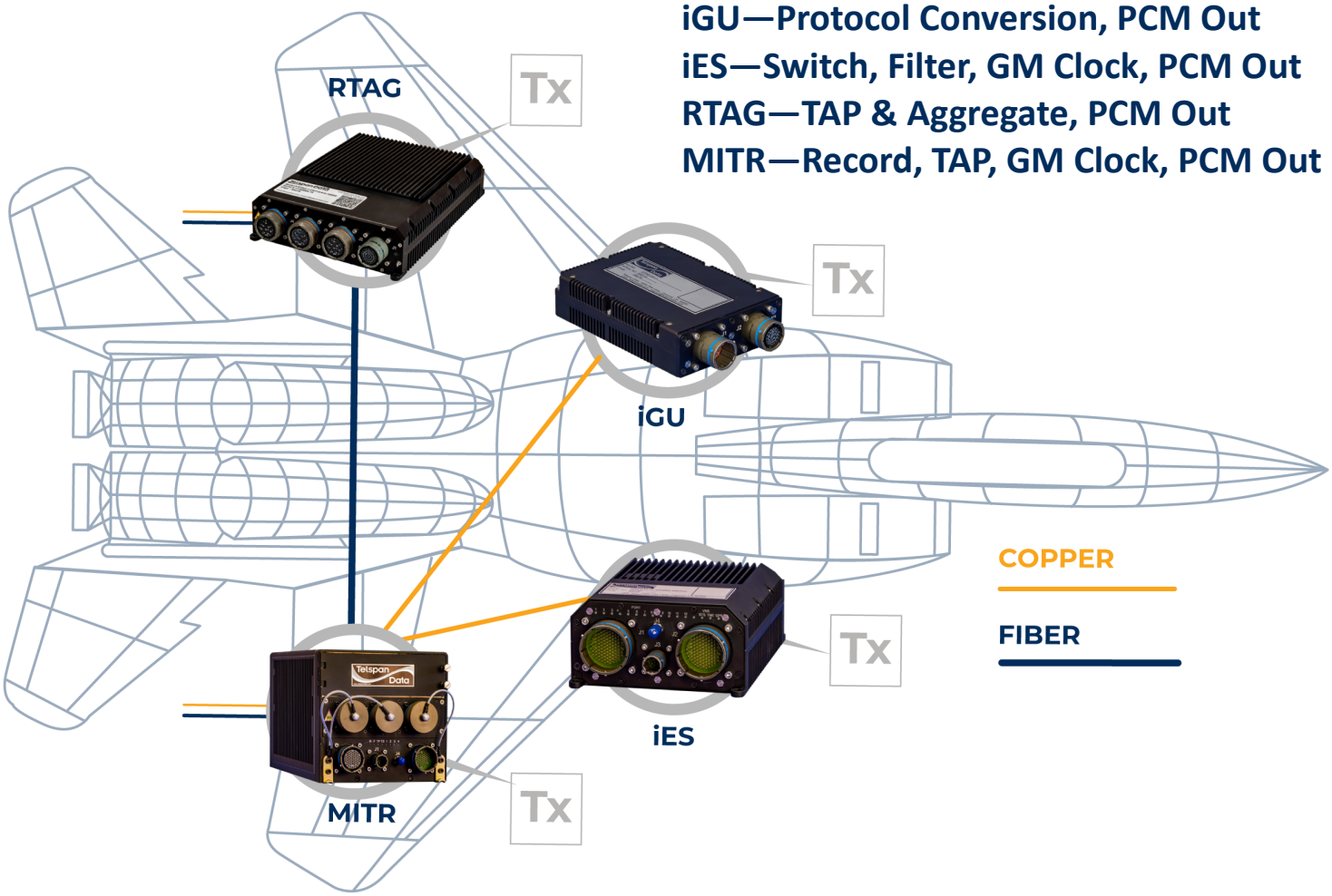
Modular Instrumentation TAP Recorder - MITR

MITR is a high-bandwidth, high-capacity processor/recorder/publisher with Optical or Copper TAP Interface Modules & high-speed removable storage. Suitable for a wide range of data recording requirements in harsh & demanding environments

Features

- Record Fibre Channel, Ethernet, AS5643 IEEE-1394B, & More
- Over 1.5GBps Sustained Recording w/ Up to 48TB of Storage
- IEEE-1588v2 Including Grand Master, IRIG TCG, GPS
- 4 Port Layer 2/3 Managed Switch w/ Recording Capability
- Chapter 7 /HDLC/ PCM Encoder/Decoder
- Simultaneous Publishing to 1-4 Ethernet Ports
- Real-Time Data Filtering

iGU—Protocol Conversion, PCM Out
iES—Switch, Filter, GM Clock, PCM Out
RTAG—TAP & Aggregate, PCM Out
MITR—Record, TAP, GM Clock, PCM Out



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